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B Alpern, CR Attanasio, JJ Barton, MG Burke, P ... - IBM Systems Journal, 2000 - research.ibm.com

... **Translation** proceeds by abstract interpretation of the ... defined by the Java **Virtual**

Machine Specification 20 ... generating the appropriate HIR **instruction(s)** and ...

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RF Cmelik, D Keppel - SIGMETRICS, 1994 - portal.acm.org

... In practice, the virtual PC is only up- dated in the **translation** epilogue, or as

needed in the **translation** body for tracing application **instruction** addresses. ...

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CHA Hsieh, JC Gyllenhaal, WH Wen-mei - Proceedings of the 29th annual ACM/IEEE international ..., 1996 - portal.acm.org

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KI Farkas, J Flinn, G Back, D Grunwald, JAM ... - SIGMETRICS, 2000 - portal.acm.org

... Execution is done by either interpret- ing each **instruction**, or by compiling the ...

In conventional sys- tems, a **virtual machine** is used in one of two modes. ...

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K Ebcioglu, ER Altman, Y Heights, N York - CONF PROC ANNU INT SYMP COMPUT ARCHIT, 1997 - ieeexplore.ieee.org

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... This static binary-**translation** framework supports ... set computers (CISC),

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K Scott, J Davidson - Proceedings of the IEEE 2001 Workshop on Binary **Translation**, 2001 - cs.utah.edu

... on the basic operation of the Strata **virtual machine**. ... Strata VM begins processing

the next application **instruction**. If a **translation** for this **instruction** has ...

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... Of primary interest, SimICS can profile data and **instruction** cache misses,

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